

**In the Claims:**

Please amend the claims as indicated below. This listing of claims replaces all prior versions.

1. (Currently Amended) A method of performing configuration or control of a subsystem that includes multiple registers that define multiple configurations of the subsystem, comprising:

providing together with the subsystem a configuration/control unit having a controller portion and a read-only storage portion storing multiple sets of configuration data, each of the sets of configuration data including configuration parameters for each of the multiple registers, and each of the sets of configuration data defining a respective one of the multiple configurations;

~~the configuration/control unit receiving an activation signal; and~~

the configuration/control unit, in response to a single register write that identifies one of the sets of configuration data ~~the activation signal,~~ encapsulating the multiple registers by performing configuration or control of the subsystem, including storing at least one of the configuration parameters of the identified set in the multiple ~~[[a]] registers of the subsystem.~~

2. (Currently Amended) The method of claim 1, wherein the subsystem is a universal serial bus (USB) block, and the multiple configurations include Control mode, Interrupt mode, Isochronous mode, and Bulk mode, each of the modes encapsulated by a single write to a common register location, and wherein the subsystem is a hardware subsystem, and the configuration/control unit is a hardware configuration/control unit.

3. (Currently Amended) The method of claim 2, ~~[[1]]~~ wherein the hardware subsystem and the hardware configuration/control unit are provided together within the same integrated circuit.

4. (Currently Amended) The method of claim 1, wherein the storing of the configuration parameters of the identified set in the multiple registers is implemented using a bus

having a width sufficient to simultaneously store the configuration parameters of the identified set-activation signal is a configuration/control ID.

5. (Currently Amended) The method of claim 1, [[4]] wherein the configuration/control unit is responsive to multiple different values for the single register write configuration/control IDs for performing different corresponding configuration or control actions with respect to the subsystem, each of the different values identifying one of the sets of configuration data.

6. (Currently Amended) A subsystem having self-configuration capabilities, comprising:  
a register section including multiple registers that define multiple configurations of [[,]] the subsystem functioning differently depending on contents of the registers; and  
a configuration/control unit having a controller portion and a read-only storage portion storing multiple sets of configuration data, each of the sets of configuration data including configuration parameters for each of the multiple registers, and each of the sets of configuration data defining a respective one of the multiple configurations; wherein the configuration/control unit is configured, responsive to a single register write an activation signal that identifies one of the sets of configuration data, for to encapsulate the multiple registers by performing configuration or control of the subsystem, including storing at least one of the configuration parameters of the identified set in one of the multiple registers registers of the subsystem.

7. (Currently Amended) The apparatus of claim 6, wherein the subsystem is a universal serial bus (USB) block, and the multiple configurations include Control mode, Interrupt mode, Isochronous mode, and Bulk mode, each of the modes encapsulated by a single write to a common register location, and wherein subsystem is a hardware subsystem, and the configuration/control unit is a hardware configuration/control unit.

8. (Previously presented) The apparatus of claim 7 wherein the hardware subsystem and the hardware configuration/control unit are provided together within the same integrated circuit.

9. (Currently Amended) The apparatus of claim 6, further comprising a bus having a width sufficient to simultaneously store the configuration parameters of the identified set in the multiple registers wherein the activation signal is a configuration/control ID.

10 (Currently Amended) The apparatus of claim 6, [[9]] wherein the configuration/control unit is responsive to multiple different values for the single register write configuration/control IDs for performing different corresponding configuration or control actions with respect to the subsystem, each of the different values identifying one of the sets of configuration data.

11. (Currently Amended) For use in a system that includes a processor coupled to a hardware subsystem via a system bus, the hardware subsystem including a configuration/control unit and a plurality of registers that define multiple configurations of the subsystem, a method of configuring the subsystem comprising:

storing a plurality of sets of configuration data parameters in a read-only memory of the configuration/control unit, each of the sets of configuration data including configuration parameters for each of the plurality of registers, and each of the sets of configuration data defining a respective one of the multiple configurations; and

responsive to the configuration/control unit receiving, from the processor, a single register write configuration/control ID that identifies one of the sets of configuration data from the processor, encapsulating the plurality of registers by writing one or more of the plurality of configuration parameters of the identified set from the read-only memory configuration/control unit to one or more of the plurality of registers.

12. (Previously presented) The method of claim 11, wherein the configuration/control unit is a state machine.

13. (Currently Amended) The method of claim 11, wherein the subsystem is a USB block comprising a plurality of ports that can operate in different modes responsive to

which of the sets of configuration data is ~~plurality of configuration parameters~~ are written to ~~which~~ of the plurality of registers.

14. (New) The method of claim 11, wherein the storing of the configuration parameters of the identified set in the plurality of registers is implemented using a bus having a width sufficient to simultaneously store the configuration parameters of the identified set.

15. (New) The method of claim 11, wherein the configuration/control unit is responsive to multiple different values for the single register write for performing different corresponding configuration or control actions with respect to the subsystem, each of the different values identifying one of the sets of configuration data.